

REMARKS

Applicant thanks the Examiner for the careful consideration given to this application. Reconsideration is now respectfully requested in view of the amendment above and the following remarks.

Claims 1-10 are pending in this application. Claim 1 is the sole independent claim. Claims 1-4 are amended. Claims 5-10 were withdrawn from consideration and are now cancelled. New Claims 11-16, which are rewritten versions of Claims 5-10, have been added. Reconsideration and allowance of the present application are respectfully requested.

Specification

The disclosure is objected to because of informalities. Applicant is providing a substitute specification, which, Applicant submits, addresses the informalities. Therefore, Applicant respectfully requests that the Examiner withdraw the objection.

Information Disclosure Statement

Applicant is filing an Information Disclosure Statement along with this response.

Claim Objections

Claims 5-10 are objected to under 37 CFR 1.75(c) as being in improper form. Applicant has elected to cancel Claims 5-10, thus rendering moot this objection. Therefore, Applicant respectfully requests that the objection to Claims 5-10 be withdrawn.

Claim Rejections under 35 U.S.C. §112

Claims 1-4 stand rejected under 35 USC §112, second paragraph, as being indefinite. This rejection is respectfully traversed.

Applicant has amended Claims 1-4 and respectfully submits that the amendments to Claims 1-4 overcome this rejection.

Therefore, Applicant respectfully requests that the rejections of Claims 1-4 under 35 U.S.C. §112 be withdrawn.

Claim Rejections under 35 U.S.C. §102

Claims 1 and 2 stand rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent Publication No. 2002/0154145 to Isakovic et al. (hereinafter "Isakovic et al."). This rejection is respectfully traversed for at least the following reasons.

Applicant first notes that Claim 1 recites, among other things, "A digital light processing (DLP) projector for the active projection of stereoscopic images, comprising: at least one digital mirror device (DMD)." The Office Action, noting page 6, cites Isakovic et al. in Fig. 6, ref. num. 20 and paragraph 105 as disclosing these components. However, Applicant respectfully submits that this fails to disclose the claimed subject matter. Ref. num. 20 of Fig. 6 does refer to a projector, as noted, e.g., in paragraph 209, which states that ref. num. 20 refers to a "light image projector." However, one issue is that paragraph 105, relied upon to disclose "comprising a DMD," does not relate to light image projector 20; rather it refers to the "two projectors" 14 and 16 of paragraph 103 and Fig. 1. Therefore, the Office Action fails to establish that projector 20 of Fig. 6, relied upon by the Office Action, comprises at least one DMD, as claimed. Additionally, paragraph 105 recites "commercially available LCD, DMD or DLP projectors," which also fails to disclose a DLP "projector...comprising: at least one...DMD," as recited in Claim 1.

Furthermore, Claim 1 not only recites, "A digital light processing (DLP) projector for the active projection of stereoscopic images, comprising: at least one digital mirror device (DMD)." Claim 1 recites further components that comprise the claimed DLP projector. Even if, *arguendo*, projector 20 were disclosed as "[a] digital light processing (DLP) projector for the active projection of stereoscopic images, comprising: at least one digital mirror device (DMD)," Isakovic et al. fails to disclose that projector 20 *comprises* the other recited elements.

Claim 1 also recites:

at least a first driver circuit interacting with a memory and controlling at least one DMD,

at least a first signal input for the input of image data of at least a first image channel and a second image channel,

at least a second driver circuit interacting with a memory; and

a switching device, which is connected on one side with the two driver circuits and on another side with at least the one DMD;

wherein the first driver circuit processes image data of the first image channel and the second driver circuit processes image data of the second image channel, and the switching device directs to the DMD, alternately, one or more images or frames from the first driver circuit, and one or more images or frames from the second driver circuit.

The Office Action, noting pages 6-7, alleges that the “at least a first driver circuit interacting with a memory” corresponds to Fig. 6, ref. nums. 22 and 104; that the “at least a second driver circuit interacting with a memory” corresponds to Fig. 6, ref. nums. 24 and 106; and that the “switching device” corresponds to Fig. 6, ref. num. 108. Office Action at 6-7. As discussed in Isakovic et al. at paragraph 207, each of ref. nums. 22 and 24 corresponds to a “graphics computer.” What is meant by a “graphics computer” is further discussed in Isakovic et al. at paragraphs 109-114. In particular, each “graphics computer” is, literally, a computer; nowhere is this referred to as a “driver.” Furthermore, Fig. 6 of Isakovic et al. shows “graphics computing unit 98” connected to projector 20. See, e.g., paragraph 209; see, also, paragraphs 163 and 187. As discussed, e.g., at paragraph 163, graphics computing unit 98 (also referred to as “graphics client unit 98”) “controls by way of its signal output operation of the projector 20.” Therefore, Applicant respectfully submits that graphics computers 22 and 24 are not “driver circuits” for projector 20; rather, Fig. 6 shows a single driver circuit for projector 20, namely, graphics computing unit 98, which is fed by “changeover switch 108.” Hence, the arrangement of Fig. 6 is similar to the prior-art system discussed in Applicant’s specification at pages 2-3 (paragraphs 9 and 15 of the substitute specification), which has a single driver circuit receiving input from a switching device (see paragraph 9), as opposed to what is claimed in Claim 1, which is a system comprising “at least a first driver circuit” and “at least a second driver circuit,” and “a switching device, which is connected on one side with the two driver circuits and on another side with at least the one DMD,” in which “the switching device directs to the DMD, alternately, one or more images or frames from the first driver circuit, and one or more images or frames from the second driver circuit.”

For at least these reasons, Applicant respectfully submits that the Office Action fails to present a *prima facie* case for the anticipation of Claim 1 by Isakovic et al. For at least the same reasons, the same is true about Claim 2.

Therefore, Applicant respectfully requests that this rejection of Claims 1 and 2 under 35 U.S.C. §102 be withdrawn.

Claim Rejections Under 35 U.S.C. §103

Claims 3 and 4 stand rejected under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent Publication No. 2002/0154145 to Isakovic et al. (hereinafter "Isakovic et al."). This rejection is respectfully traversed for at least the following reasons.

The Office Action relies on the same reference (Isakovic et al.) alleged to anticipate Claims 1 and 2. The Office Action, noting page 8, does not provide any further discussion of the elements found in Claims 1 and 2, so it is understood that the same rationale is used as was presented for the rejections under 35 U.S.C. § 102 at pages 6-7 of the Office Action. Therefore, the above discussion also applies to the rejections of Claims 3 and 4, and for at least the reasons presented above, the Office Action fails to present a *prima facie* case for the obviousness of Claims 3 and 4 based on Isakovic et al.

Therefore, Applicant respectfully requests that this rejection of Claims 3 and 4 under 35 U.S.C. §103 be withdrawn.

Disclaimer

Applicant may not have presented all possible arguments or have refuted the characterizations of either the claims or the prior art as found in the Office Action. However, the lack of such arguments or refutations is not intended to act as a waiver of such arguments or as concurrence with such characterizations.

CONCLUSION

In view of the above, consideration and allowance are respectfully solicited.

In the event the Examiner believes an interview might serve in any way to advance the prosecution of this application, the undersigned is available at the telephone number noted below.

The Office is authorized to charge any necessary fees to Deposit Account No. 22-0185.

Applicant believes no fee is due with this response. However, if a fee is due, please charge our Deposit Account No. 22-0185, under Order No. 22407-00047-US from which the undersigned is authorized to draw.

Dated: November 12, 2010

Respectfully submitted,

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ANNOTATED SPECIFICATION – 10/578,760

FIELD OF THE INVENTION

[0001] The invention relates to a DLP-projector for the active projection of stereoscopic images. The projector includes a DMD, at least a first driver circuit interacting with a memory and serving for controlling at least one DMD, and at least a first signal input for the input of image data of at least a first image channel and a second image channel.

BACKGROUND

[0002] For the stereoscopic presentation of images, there are two basic methods, namely active projection and passive projection.

[0003] In the case of passive projection, the first and second images of a stereoscopic image pair are presented simultaneously on a projection screen. Separation of the images is done using filters, for example polarizing filters. The observer wears glasses with appropriate filters, in order to see one image with one eye and the other image with the other eye.

[0004] In the case of the active projection technology, the two images are presented sequentially, thus, one after the other in time. For observing the two images, shutter-glasses are used, which alternately close the view for one of the two eyes, in unison with the changing of the images. In order to be able to view the images flicker-free, high repetition frequencies are required.

[0005] WO 95/18511 discloses a method and apparatus for effecting the recording and active playback of stereoscopic video images of observed scenes, wherein the recording, or storing, of images in conventional data storage media is made easier by the performing of a half-image detection before, or after, the recording, and by determining therewith the association of the relevant half-images to the corresponding frames of left and right channels, or the association of two frames in parallel channels, as the case may be. In this system, complete images of the right and left channels are alternately presented, one after

the other, on a CRT-monitor. U.S. Pat. No. 4,954,890 discloses a passive projection system with two mutually separated, optical systems, each having a light source and an LCD-panel, which cast left and right images onto a display screen, wherein a right, current image is placed on a left, previous image, and vice versa, in order to improve the brightness of the image, especially in comparison to conventional, previously known, active systems, in which right and left images are presented alternately on a CRT-monitor. To this end, there is connected before the image storing electronics of the LCD-panel a demultiplexer, which distributes the signals of a signal source channel-wise onto the image storer. Disadvantageous in this system is the requirement for two LCD-panels and the relatively high costs associated therewith.

[0006] The image repetition rate of LCD-panels is limited, as a rule, to 60 Hz, due to their internal, image storing electronics, so that they are not suited for active projection of stereoscopic images.

[0007] DLP-projectors are known. DLP stands for Digital Light Processing. Basis of this technology is an about thumbnail-sized chip, a so-called Digital Mirror Device (DMD), developed by the firm Texas Instruments. Depending on image resolution, typically 800.times.600, 1024.times.768 or 1280.times.1024, or even higher, microscopically small mirrors are placed on the chip. For every individual pixel, the mirrors reflect a projection light coming from a light source, so that an image identical with a data source is formed on a projection surface. The mirrors can be shifted electronically up to 50,000 times in a second, so that all pixels can be turned on and off up to 25,000 times in a second. Installed between the mirrors and the light source is a rapidly rotating, color wheel, which typically produces 3, or 4, color images in the sequence red/blue/green, or red/blue/green/white, as the case may be. Due to the rapid repetition frequency and the inertia of the human eye, these are perceived as a one image.

[0008] Various efforts have been made towards using such DLP-projectors for the projection of stereoscopic images.

[0009] DE 1 016 01 60A1 discloses a method for active, stereoscopic projection using a DLP projector of the above-described type. The projector has a first driver circuit for controlling a DMD. The driver circuit cooperates with, in each case, two memory components, which, in each case, have two memory banks and are connected with the driver circuit via a switching device, a so-called crossbar, which controls the data flow from the memory banks to the DLP-memory-bus-system. In such case, the image data of an image of a first or second image channel are written to, or read-out from, the separate memory banks during four color-wheel rotations, selectively in a certain cycle. The writing of the memory banks with the image data of the first and second image channels requires the duration of four color-wheel rotations. And, these data, during four color-wheel rotations, are, in each case, twice read-out of the memory banks and fed via the driver circuit to the DMD. Serving as signal input of the DLP-projector is a so-called interface-board.

[0010] The demands on the switching devices with respect to addressing of the individual memory banks and addresses is comparatively complex and can, especially in the case of higher system clock rates, lead to difficulties.

SUMMARY OF EMBODIMENTS OF THE INVENTION

[0011] An object of the present invention is to simplify the control of a single-chip DMD for the projection of a stereo-image.

[0012] This object is achieved in the case of a DLP-projector of the above-described type through application of at least a second driver circuit cooperating with a memory and by a switching device connected on one side with the two driver circuits and on its other side with at least the one DMD, with the first driver circuit processing image data of the first image channel and the second driver circuit processing image data of the second image channel, and with the switching device feeding to the DMD alternately one or more images or frames from the first driver circuit and one or more images or frames from the second driver circuit.

[0013] The term "switching device" is meant herein to include any switching system, with which a plurality of data sources can be switched to one or more data sinks. In the simplest case, the data of two driver circuits are alternately switched onto a data bus. Also included under the term, however, is also a switching system, by which more than two data sources can be switched onto one or more data sinks.

[0014] The term "signal inputs" is meant herein to include both inputs for analog signals, which are then converted to digital form, as well as also inputs for digital signals.

[0015] A core idea of the invention is to not process the image data of the first and second image channels in a first driver circuit, but, instead, to provide a driver circuit for each image channel, with the switching device caring for directing to the DMD, in a predetermined sequence, first the data of an image or frame of the first image channel of the first driver circuit and then the data of an image or frame of the second image channel of the second driver circuit. In this way, the inherent limitation of the system known from DE 1 016 01 60A1, in which the image data of both image channels must be processed by one driver circuit, is avoided, in that, now, the processing of the image data of the individual image channels occurs in parallel and the DMD is fed only thereafter, in sequential order.

[0016] The switching device is, in such case, significantly simplified, in that it then no longer has to control the cycle of reading and writing accesses to different memory banks, but, instead, only must connect the data lines of the data outputs of the different driver systems alternately with the DMD. An actuating of address regions is omitted completely in such case. Also the demands on the clocking of the switching device become smaller in this way, since it must now occur only in the rhythm of the image change at the DMD and, thus, in the case of DMDs of today, does not, as a rule, exceed 120 Hz. Finally, the data of, in each case, one image can be made available within two color-wheel rotations, because of the parallel processing of the data of the different image channels. In this way, a significantly more flicker-free projection of a series of stereoscopic images becomes possible, this meaning, as well, a better quality of the

projection.

[0017] When the switching device, as provided in the system of the invention, is arranged only at the end of the image processing pipeline, then a maximum image frequency and quality of stereoscopic images to be actively presented with the DLP projector are possible. Also, it becomes possible with this type of system to direct, in simple manner, more than two image channels to the DMD, with, as required, also more DMDs being connectable to the data bus of the system, with the image channels then being distributable in sensible manner to the DMDs.

[0018] Basically, the image data of the image channels, thus, especially, the image data of the first and second, or right and left, image channels, can be received sequentially, one after the other, via exactly one signal input. In this case, the image data of the respective image channels must be distributed into the driver circuits via an appropriate logic assigned to the signal input. In order that a stereoscopic image, or image sequence, with stereoscopic images can be projected flicker-free, the logic for producing the image data, for example a computer with a graphics, or video, card, must be able to make the image data available sufficiently fast enough. It can, therefore, be of advantage to provide at least a second signal input, with the first signal input receiving the image signals of the first stereo channel and forwarding such into the first driver circuit, and the second signal input receiving the image signals of the second stereo channel and forwarding such into the second driver circuit. Thus, the data of the individual image channels can be directed to the DLP-projector in parallel with one another.

[0019] To the extent that the data of the respective image channels are directed synchronized to the DLP-projector, it is possible, as required, to omit a synchronizing of the image signals of the individual image channels. If such is not so, then it is necessary that the DLP-projector includes a clock mechanism for synchronizing the image signals. In such case, it can, for example, be of advantage to synchronize the signal inputs and/or the driver circuits with one another by use of a clock signal.

[0020] Another preferred form of embodiment provides a microprocessor, which controls the switching device and/or produces a control signal for shutter-glasses. Such a microprocessor is especially necessary, when the data of the respective image channels are sent to the DLP-projector unsynchronized or are not processed synchronously within the DLP-projector. If the synchronizing of the data of the image channels occurs outside of the projector in such a manner that also the switching clock of the switching device and thus of the image changes of the one or more DMDs can be derived therefrom, then also the control of shutter-glasses can be effected via a clock signal available outside of the DLP-projector for the synchronizing of the data, for example then, when a plurality of DLP-projectors are connected in parallel and a projector has an image change signal output, via which the parallel-connected projectors obtain the signal for image change for the respective switching devices.

[0021] The greatest color depth in the case of projection of stereoscopic images is currently obtainable with driver circuits based on an ASIC of type DDP 1000 of the firm Texas Instruments and such circuits are, for that reason, preferred.

[0022] The switching device can, fundamentally, connect all signal inputs and outputs of the DMD with the corresponding signal inputs and outputs of, in each case, one driver circuit, and then switch all signal lines over onto another driver circuit. It is simpler, however, when the switching device connects all signal lines with a first driver circuit and only switches the data lines of the DMD over onto the respective driver circuits.

[0023] In another preferred embodiment, for the projection of the image data of an image channel as a mono-image, or mono-image sequence, the switching device connects the data lines of the DMD over a desired period of time only with the corresponding data lines of one of the driver circuits. To this end, a control logic can, for example, be provided, which, via a corresponding input of a user of the projector, "freezes" the switch position of the switching device on one image channel, so that a mono-projection of the selected image channel occurs, as required, at full image repetition frequency of the DMD. A shutter-glasses can, via a correspondingly provided logic for this type of

operation, be so controlled, that both "lenses" remain open, so that the observer can observe the mono-images with both eyes.

[0024] In reverse fashion, in another preferred type of operation, it can be provided that the images of an image channel can be played, or interspersed, over an arbitrarily short period of time, into an image sequence of another image channel. In this type of operation, it can be of advantage to control a shutter-glasses in such a manner that both "lenses" are closed during the projection of the images which were played, or interspersed, into the image sequence of the other image channel. A shutter-glasses control of this type can, for example, be of advantage, when, for example, in the projection of virtual worlds, three-dimensional objects located in the space are to be measured via the projection of the interspersed images onto these objects, without the observer having any indication that this is being done. The measurement of three-dimensional objects via a projection of grids onto these objects is, *per se*, within the ability of those skilled in the art and is not explained further here.

[0025] Finally, for reasons of simpler construction of the DLP-projector, it is of advantage to arrange the driver circuits and switching device on a circuit board.

BRIEF DESCRIPTION OF THE DRAWING

[0026] The invention will now be explained in greater detail on the basis of a block diagram in the sole FIGURE of the drawing presenting a preferred example of an embodiment of the invention.

DETAILED DESCRIPTION OF EMBODIMENTS OF THE INVENTION

[0027] FIG. 1 shows the individual circuit components needed for the electronic data processing of the image data. Provided as signal inputs are two interface boards 1, 1', which are connected in parallel with one another. Interface boards 1, 1' can alternatively receive analog or digital image data. Analog image data are converted into digital image data by A/D-converters on the interface boards 1, 1'.

[0028] The interface boards 1, 1' are synchronized via a synchronizing device 2, including a clock. In this, the image data of an image or frame of an image channel are buffered in a memory on the interface board and only read-out upon receipt of a synchronizing signal.

[0029] In the data flow direction, following each interface board 1, 1', a formatter board 3, 3' is arranged. Each formatter board 3, 3' receives from its foregoing interface board 1, 1' both digital image data and also a clock signal (Clock). On the formatter boards 3, 3' are driver circuits for controlling the DMD 4 downstream in the data flow direction. The driver circuits include, in each case, an ASIC 5, 5', as well as, in each case, a memory 6, 6' interacting with the ASIC 5, 5'.

[0030] The outputs of the formatter boards 1, 1' go to a switching device 7, which connects the data outputs of the formatter boards 3, 3' with the DMD.

[0031] A preferred ASIC for controlling the DMD is a DDP 1000 of the firm Texas Instruments.

[0032] The switching device 7 is so embodied, that all control lines of the ASIC 5, which are not image data lines and which are needed for control of the DMD 4, are looped through and are connected via the switching device 7 directly with the DMD 4. In contrast, the outputs of the ASIC 5, on which the image data are directed, are switched via the switching device to the DMD 4 in alternation with the data outputs of the ASIC 5' of the parallel-connected formatter board 3'.

[0033] The illustrated switching arrangement can be modified in many ways, without departing from the fundamental features of the invention. Thus, for example, other series-connected interface boards and formatter boards can be arranged in parallel with the illustrated boards, so that more than only two image channels can be processed and switched onto the DMD via a correspondingly modified switching device. The synchronizing of the image data processing does not have to occur on the interface

boards; the formatter boards can also be synchronized directly via a clock, or a synchronizing in the electronics of the DLP-projector can be completely omitted, when the image data of the individual image channels are fed to the interface boards already in synchronized form. When the formatter boards are arranged on a circuit board, it can be sufficient or even especially advantageous, when two or more driver-circuit ASICs use a memory which they share together. Furthermore, a plurality of DMDs can be provided, so that the switching device distributes a number of M image channels to N DMDs.

WHAT IS CLAIMED IS:

SUBSTITUTE SPECIFICATION – 10/578,760

FIELD OF THE INVENTION

[0001] The invention relates to a DLP-projector for the active projection of stereoscopic images. The projector includes a DMD, at least a first driver circuit interacting with a memory and serving for controlling at least one DMD, and at least a first signal input for the input of image data of at least a first image channel and a second image channel.

BACKGROUND

[0002] For the stereoscopic presentation of images, there are two basic methods, namely active projection and passive projection.

[0003] In the case of passive projection, the first and second images of a stereoscopic image pair are presented simultaneously on a projection screen. Separation of the images is done using filters, for example polarizing filters. The observer wears glasses with appropriate filters, in order to see one image with one eye and the other image with the other eye.

[0004] In the case of the active projection technology, the two images are presented sequentially, thus, one after the other in time. For observing the two images, shutter-glasses are used, which alternately close the view for one of the two eyes, in unison with the changing of the images. In order to be able to view the images flicker-free, high repetition frequencies are required.

[0005] WO 95/18511 discloses a method and apparatus for effecting the recording and active playback of stereoscopic video images of observed scenes, wherein the recording, or storing, of images in conventional data storage media is made easier by the performing of a half-image detection before, or after, the recording, and by determining therewith the association of the relevant half-images to the corresponding frames of left and right channels, or the association of two frames in parallel channels, as the case may be. In this system, complete images of the right and left channels are alternately presented, one after

the other, on a CRT-monitor. U.S. Pat. No. 4,954,890 discloses a passive projection system with two mutually separated, optical systems, each having a light source and an LCD-panel, which cast left and right images onto a display screen, wherein a right current image is placed on a left, previous image, and vice versa, in order to improve the brightness of the image, especially in comparison to conventional, previously known, active systems, in which right and left images are presented alternately on a CRT-monitor. To this end, there is connected before the image storing electronics of the LCD-panel a demultiplexer, which distributes the signals of a signal source channel-wise onto the image storer. Disadvantageous in this system is the requirement for two LCD-panels and the relatively high costs associated therewith.

[0006] The image repetition rate of LCD-panels is limited, as a rule, to 60 Hz, due to their internal, image storing electronics, so that they are not suited for active projection of stereoscopic images.

[0007] DLP-projectors are known. DLP stands for Digital Light Processing. Basis of this technology is an about thumbnail-sized chip, a so-called Digital Mirror Device (DMD), developed by the firm Texas Instruments. Depending on image resolution, typically 800.times.600, 1024.times.768 or 1280.times.1024, or even higher, microscopically small mirrors are placed on the chip. For every individual pixel, the mirrors reflect a projection light coming from a light source, so that an image identical with a data source is formed on a projection surface. The mirrors can be shifted electronically up to 50,000 times in a second, so that all pixels can be turned on and off up to 25,000 times in a second. Installed between the mirrors and the light source is a rapidly rotating, color wheel, which typically produces 3, or 4, color images in the sequence red/blue/green, or red/blue/green/white, as the case may be. Due to the rapid repetition frequency and the inertia of the human eye, these are perceived as a one image.

[0008] Various efforts have been made towards using such DLP-projectors for the projection of stereoscopic images.

[0009] DE 1 016 01 60A1 discloses a method for active, stereoscopic projection using a DLP projector of the above-described type. The projector has a first driver circuit for controlling a DMD. The driver circuit cooperates with, in each case, two memory components, which, in each case, have two memory banks and are connected with the driver circuit via a switching device, a so-called crossbar, which controls the data flow from the memory banks to the DLP-memory-bus-system. In such case, the image data of an image of a first or second image channel are written to, or read-out from, the separate memory banks during four color-wheel rotations, selectively in a certain cycle. The writing of the memory banks with the image data of the first and second image channels requires the duration of four color-wheel rotations. And, these data, during four color-wheel rotations, are, in each case, twice read-out of the memory banks and fed via the driver circuit to the DMD. Serving as signal input of the DLP-projector is a so-called interface-board.

[0010] The demands on the switching devices with respect to addressing of the individual memory banks and addresses is comparatively complex and can, especially in the case of higher system clock rates, lead to difficulties.

SUMMARY OF EMBODIMENTS OF THE INVENTION

[0011] An object of the present invention is to simplify the control of a single-chip DMD for the projection of a stereo-image.

[0012] This object is achieved in the case of a DLP-projector of the above-described type through application of at least a second driver circuit cooperating with a memory and by a switching device connected on one side with the two driver circuits and on its other side with at least the one DMD, with the first driver circuit processing image data of the first image channel and the second driver circuit processing image data of the second image channel, and with the switching device feeding to the DMD alternately one or more images or frames from the first driver circuit and one or more images or frames from the second driver circuit.

[0013] The term "switching device" is meant herein to include any switching system, with which a plurality of data sources can be switched to one or more data sinks. In the simplest case, the data of two driver circuits are alternately switched onto a data bus. Also included under the term, however, is also a switching system, by which more than two data sources can be switched onto one or more data sinks.

[0014] The term "signal inputs" is meant herein to include both inputs for analog signals, which are then converted to digital form, as well as also inputs for digital signals.

[0015] A core idea of the invention is to not process the image data of the first and second image channels in a first driver circuit, but, instead, to provide a driver circuit for each image channel, with the switching device caring for directing to the DMD, in a predetermined sequence, first the data of an image or frame of the first image channel of the first driver circuit and then the data of an image or frame of the second image channel of the second driver circuit. In this way, the inherent limitation of the system known from DE 1 016 01 60A1, in which the image data of both image channels must be processed by one driver circuit, is avoided, in that, now, the processing of the image data of the individual image channels occurs in parallel and the DMD is fed only thereafter, in sequential order.

[0016] The switching device is, in such case, significantly simplified, in that it then no longer has to control the cycle of reading and writing accesses to different memory banks, but, instead, only must connect the data lines of the data outputs of the different driver systems alternately with the DMD. An actuating of address regions is omitted completely in such case. Also the demands on the clocking of the switching device become smaller in this way, since it must now occur only in the rhythm of the image change at the DMD and, thus, in the case of DMDs of today, does not, as a rule, exceed 120 Hz. Finally, the data of, in each case, one image can be made available within two color-wheel rotations, because of the parallel processing of the data of the different image channels. In this way, a significantly more flicker-free projection of a series of stereoscopic images becomes possible, this meaning, as well, a better quality of the

projection.

[0017] When the switching device, as provided in the system of the invention, is arranged only at the end of the image processing pipeline, then a maximum image frequency and quality of stereoscopic images to be actively presented with the DLP projector are possible. Also, it becomes possible with this type of system to direct, in simple manner, more than two image channels to the DMD, with, as required, also more DMDs being connectable to the data bus of the system, with the image channels then being distributable in sensible manner to the DMDs.

[0018] Basically, the image data of the image channels, thus, especially, the image data of the first and second, or right and left, image channels, can be received sequentially, one after the other, via exactly one signal input. In this case, the image data of the respective image channels must be distributed into the driver circuits via an appropriate logic assigned to the signal input. In order that a stereoscopic image, or image sequence, with stereoscopic images can be projected flicker-free, the logic for producing the image data, for example a computer with a graphics, or video, card, must be able to make the image data available sufficiently fast enough. It can, therefore, be of advantage to provide at least a second signal input, with the first signal input receiving the image signals of the first stereo channel and forwarding such into the first driver circuit, and the second signal input receiving the image signals of the second stereo channel and forwarding such into the second driver circuit. Thus, the data of the individual image channels can be directed to the DLP-projector in parallel with one another.

[0019] To the extent that the data of the respective image channels are directed synchronized to the DLP-projector, it is possible, as required, to omit a synchronizing of the image signals of the individual image channels. If such is not so, then it is necessary that the DLP-projector includes a clock mechanism for synchronizing the image signals. In such case, it can, for example, be of advantage to synchronize the signal inputs and/or the driver circuits with one another by use of a clock signal.

[0020] Another preferred form of embodiment provides a microprocessor, which controls the switching device and/or produces a control signal for shutter-glasses. Such a microprocessor is especially necessary, when the data of the respective image channels are sent to the DLP-projector unsynchronized or are not processed synchronously within the DLP-projector. If the synchronizing of the data of the image channels occurs outside of the projector in such a manner that also the switching clock of the switching device and thus of the image changes of the one or more DMDs can be derived therefrom, then also the control of shutter-glasses can be effected via a clock signal available outside of the DLP-projector for the synchronizing of the data, for example then, when a plurality of DLP-projectors are connected in parallel and a projector has an image change signal output, via which the parallel-connected projectors obtain the signal for image change for the respective switching devices.

[0021] The greatest color depth in the case of projection of stereoscopic images is currently obtainable with driver circuits based on an ASIC of type DDP 1000 of the firm Texas Instruments and such circuits are, for that reason, preferred.

[0022] The switching device can, fundamentally, connect all signal inputs and outputs of the DMD with the corresponding signal inputs and outputs of, in each case, one driver circuit, and then switch all signal lines over onto another driver circuit. It is simpler, however, when the switching device connects all signal lines with a first driver circuit and only switches the data lines of the DMD over onto the respective driver circuits.

[0023] In another preferred embodiment, for the projection of the image data of an image channel as a mono-image, or mono-image sequence, the switching device connects the data lines of the DMD over a desired period of time only with the corresponding data lines of one of the driver circuits. To this end, a control logic can, for example, be provided, which, via a corresponding input of a user of the projector, "freezes" the switch position of the switching device on one image channel, so that a mono-projection of the selected image channel occurs, as required, at full image repetition frequency of the DMD. A shutter-glasses can, via a correspondingly provided logic for this type of

operation, be so controlled, that both "lenses" remain open, so that the observer can observe the mono-images with both eyes.

[0024] In reverse fashion, in another preferred type of operation, it can be provided that the images of an image channel can be played, or interspersed, over an arbitrarily short period of time, into an image sequence of another image channel. In this type of operation, it can be of advantage to control a shutter-glasses in such a manner that both "lenses" are closed during the projection of the images which were played, or interspersed, into the image sequence of the other image channel. A shutter-glasses control of this type can, for example, be of advantage, when, for example, in the projection of virtual worlds, three-dimensional objects located in the space are to be measured via the projection of the interspersed images onto these objects, without the observer having any indication that this is being done. The measurement of three-dimensional objects via a projection of grids onto these objects is, *per se*, within the ability of those skilled in the art and is not explained further here.

[0025] Finally, for reasons of simpler construction of the DLP-projector, it is of advantage to arrange the driver circuits and switching device on a circuit board.

BRIEF DESCRIPTION OF THE DRAWING

[0026] The invention will now be explained in greater detail on the basis of a block diagram in the sole FIGURE of the drawing presenting a preferred example of an embodiment of the invention.

DETAILED DESCRIPTION OF EMBODIMENTS OF THE INVENTION

[0027] FIG. 1 shows the individual circuit components needed for the electronic data processing of the image data. Provided as signal inputs are two interface boards 1, 1', which are connected in parallel with one another. Interface boards 1, 1' can alternatively receive analog or digital image data. Analog image data are converted into digital image data by A/D-converters on the interface boards 1, 1'.

[0028] The interface boards 1, 1' are synchronized via a synchronizing device 2, including a clock. In this, the image data of an image or frame of an image channel are buffered in a memory on the interface board and only read-out upon receipt of a synchronizing signal.

[0029] In the data flow direction, following each interface board 1, 1', a formatter board 3, 3' is arranged. Each formatter board 3, 3' receives from its foregoing interface board 1, 1' both digital image data and also a clock signal (Clock). On the formatter boards 3, 3' are driver circuits for controlling the DMD 4 downstream in the data flow direction. The driver circuits include, in each case, an ASIC 5, 5' as well as, in each case, a memory 6, 6' interacting with the ASIC 5, 5'.

[0030] The outputs of the formatter boards 1, 1' go to a switching device 7, which connects the data outputs of the formatter boards 3, 3' with the DMD.

[0031] A preferred ASIC for controlling the DMD is a DDP 1000 of the firm Texas Instruments.

[0032] The switching device 7 is so embodied, that all control lines of the ASIC 5, which are not image data lines and which are needed for control of the DMD 4, are looped through and are connected via the switching device 7 directly with the DMD 4. In contrast, the outputs of the ASIC 5, on which the image data are directed, are switched via the switching device to the DMD 4 in alternation with the data outputs of the ASIC 5' of the parallel-connected formatter board 3'.

[0033] The illustrated switching arrangement can be modified in many ways, without departing from the fundamental features of the invention. Thus, for example, other series-connected interface boards and formatter boards can be arranged in parallel with the illustrated boards, so that more than only two image channels can be processed and switched onto the DMD via a correspondingly modified switching device. The synchronizing of the image data processing does not have to occur on the interface

boards; the formatter boards can also be synchronized directly via a clock, or a synchronizing in the electronics of the DLP-projector can be completely omitted, when the image data of the individual image channels are fed to the interface boards already in synchronized form. When the formatter boards are arranged on a circuit board, it can be sufficient or even especially advantageous, when two or more driver-circuit ASICs use a memory which they share together. Furthermore, a plurality of DMDs can be provided, so that the switching device distributes a number of M image channels to N DMDs.

WHAT IS CLAIMED IS: